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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

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\*\* FOREIGN APPLICATIONS \*\*\*\*\*

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Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	<i>GCR 9/16/04</i>	COUNTRY	DRAWING	CLAIMS
Verified and Acknowledged	Examiner's Signature <i>GCR 9/16/04</i> Initials	MN	6	33	7

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TITLE

Memory bus polarity indicator system and method for reducing the affects of simultaneous switching outputs (SSO) on memory bus timing

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